HÖRILIN et al Serial No. 10/629,773

Atty Dkt: 2380-788 Art Unit: 2663

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THE CLAIMS:

The following is listing of pending claims (not amended by this document):

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- 41. (Previously Presented) A method of operating a node of a telecommunications system, the node comprising a switch core, the switch core comprising a buffer matrix having one buffer memory per crosspoint to which cells having differing priority classes are written; the method comprising:

providing an indication that a high priority cell is in a queue awaiting writing to the buffer memory at a crosspoint blocked by a low priority cell;

emptying the blocked crosspoint in response to the indication.

- 42. (Previously Presented) The method of claim 41, further comprising increasing the priority of the low priority cell in response to the indication.
- 43. (Previously Presented) The method of claim 41, further comprising detecting the indication in a management cell received at the switch core.

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